

REMARKS

Applicant appreciates the Examiner's thorough consideration provided the present application. Claims 1-21 are now present in the application. Claims 1 and 19 are independent. Reconsideration of this application is respectfully requested.

Failure To Comply With MPEP §707.07(f)

This Office Action is premature because it does not comply with MPEP §707.07(f), which requires that the Examiner respond on the merits to the substance of each of the arguments presented by Applicant traversing rejections of record.

The outstanding Office Action never responds to the merits of each of the specific arguments presented by Applicant traversing the rejection of claim 1, found on page 10, lines 15-22 and on page 11, lines 1-6 of the Amendment filed November 10, 2005, in violation of MPEP §707.02(f). This denies Applicant fundamental substantive and procedural process under the Administrative Procedures Act. See in this regard, In re Zurko, 119 S.Ct. 1816, 50 USPQ2d 1930 (1999), and In re Gartside, 53 USPQ2d 1769 (Fed. Cir. 2000).

In particular, on page 10, lines 15-22 and on page 11, lines 1-6 of the Amendment filed November 10, 2005, Applicant presented the arguments that the output DA-i of the digital-analog converter DAC in Koyama is a *voltage* signal and depends on the graduation power supply VR and the logical operations of the count signals C0-2, the set signal ST, and the signals L-i(0)-(2), and therefore Koyama nowhere teaches using a current mirror method to supply *current of a plurality of levels* to a data line of the display panel according to logical combinations of the sampled picture signal from the latch unit as recited in claim 1.

However, the above-noted arguments are not addressed in the "Response to Arguments" section of the outstanding Office Action, thereby denying Applicants fundamental substantive and procedural due process under the Administrative Procedures Act. Applicant respectfully submits that the above-noted arguments were presented in good faith and deserve to be considered and responded to on their merits.

Reconsideration and withdrawal of the finality of the outstanding Office Action is respectfully requested.

Allowable Subject Matter

The Examiner has indicated that dependent claims 5-15 and 18 would be allowable if rewritten to include all of the limitations of the base claim and any intervening claims. Applicant appreciates the indication of allowable subject matter by the Examiner.

Drawings

The Examiner did not indicate whether or not the formal drawings have been accepted. Since no objection has been received, Applicants assume that the drawings are acceptable and that no further action is necessary. Confirmation thereof in the next Office Action is respectfully requested.

Claim Rejections Under 35 U.S.C. § 102

Claims 1-4, 16, 17 and 19-21 stand rejected under 35 U.S.C. § 102(b) as being anticipated by Koyama, U.S. Patent Application Publication No. US 2001/0048408. This rejection is respectfully traversed.

A complete discussion of the Examiner's rejection is set forth in the Office Action, and is not being repeated here.

Independent claim 1 recites "a voltage to current converting unit supplying current of a plurality of levels to a data line of the display panel according to logical combinations of the sampled picture signal from the latch unit, using a current mirror method".

Independent claim 19 recites "a plurality of pixel units located on the substrate" and "a data driving circuit located on the same substrate, the data driving circuit including a plurality of current paths, the data driving circuit supplying current of a plurality of levels to at least one of the plurality of pixel units by providing the current from at least one of the plurality of current paths".

Applicant respectfully submits that the combinations of elements set forth in claims 1 and 19 are not disclosed or suggested by the references relied on by the Examiner.

Claim 1

The Examiner alleged that Koyama in FIGs. 19, 24 and paragraphs 0200-0208 and 0223-0224 and in FIGs. 1, 3, 9, 10 and paragraphs 0095-0107, 0182-0208 and 0222-0225 discloses using a *current mirror method* to supply current of a plurality of levels to a data line of the

display panel according to logical combinations of the sampled picture signal from the latch unit as recited in claim 1. Applicant respectfully disagrees.

In particular, the paragraphs 0223-0224 of Koyama, to which the Examiner referred, simply disclose a mirror 3703 for a rear-type projector (see FIG. 24B), and a dichroic mirror 3803 and mirrors 3802, 3804-3806 for a projection device (see FIG. 24C). As shown in FIGs. 24B and 24C, the mirrors are simply *optical mirrors* to reflect the light beams. Koyama's mirrors are merely flat surfaces that reflect the light. They are *not current mirrors* to provide any current, and have nothing to do with the driving circuit of the flat panel display. According, those optical mirrors cannot be relied on by the Examiner to teach "a voltage to current converting unit supplying current of a plurality of levels to a data line of the display panel according to logical combinations of the sampled picture signal from the latch unit, using a *current mirror method*" as recited in claim 1.

Koyama in the other referred paragraphs and drawings also fails to teach using any *current mirror method* as recited in claim 1. For example, FIGs. 19A and 19B of Koyama simply disclose the pixel units of the EL display device without addressing any driving circuit or voltage to current converting unit. FIGs. 1, 3, 9 and 10 simply disclose the data driving circuit without motioning using any current mirror method. Therefore, Koyama fails to teach "a voltage to current converting unit supplying current of a plurality of levels to a data line of the display panel according to logical combinations of the sampled picture signal from the latch unit, using a *current mirror method*" as recited in claim 1.

In the alternative, as mentioned, Applicant on page 10, lines 15-22 and on page 11, lines 1-6 of the Amendment filed November 10, 2005 argued that the output DA-i of the digital-

analog converter DAC in Koyama is a *voltage* signal and depends on the graduation power supply VR and the logical operations of the count signals C0-2, the set signal ST, and the signals L-i(0)-(2). Since the output of the digital-analog converter DAC in Koyama is a *voltage* signal, not a current signal, Koyama fails to teach using a current mirror method to supply *current of a plurality of levels* to a data line of the display panel according to logical combinations of the sampled picture signal from the latch unit as recited in claim 1.

Claim 19

The Examiner alleged that Koyama discloses “a plurality of pixel units located on the substrate” and “a data driving circuit located on the same substrate, the data driving circuit including a plurality of current paths, the data driving circuit supplying current of a plurality of levels to at least one of the plurality of pixel units by providing the current from at least one of the plurality of current paths” as recited in claim 19. However, the Examiner on page 4, lines 1-6 of his Office Action simply repeated the same claim language of claim 19 without specifying where and how Koyama teaches the features of claim 19. Such a conclusory statement, standing alone, cannot be used against claim 19. In fact, Koyama nowhere discloses that the pixel units and the data driving circuit are located *on the same substrate* as recited in claim 19. If the Examiner persists in his rejection of claim 19, it is respectfully requested that the Examiner indicate where and how Koyama teaches the features of claim 19 in next Office Action.

Since Koyama fails to teach at least the above-noted features of independent claims 1 and 19, Applicant respectfully submits that claims 1 and 19 and their dependent claims (due to their

dependency) are patentable over Koyama. Accordingly, reconsideration and withdrawal of the rejection under 35 U.S.C. § 102 are respectfully requested.

CONCLUSION

All the stated grounds of rejection have been properly traversed and/or rendered moot. Applicant therefore respectfully requests that the Examiner reconsider all presently pending rejections and that they be withdrawn.

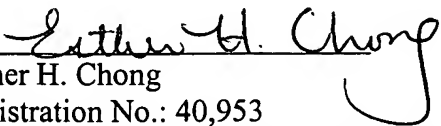
It is believed that a full and complete response has been made to the Office Action, and that as such, the Examiner is respectfully requested to send the application to Issue.

In the event there are any matters remaining in this application, the Examiner is invited to contact the undersigned at (703) 205-8000 in the Washington, D.C. area.

If necessary, the Commissioner is hereby authorized in this, concurrent, and future replies, to charge payment or credit any overpayment to Deposit Account No. 02-2448 for any additional fees required under 37 C.F.R. §§1.16 or 1.17; particularly, extension of time fees.

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Respectfully submitted,

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